



Internship @CVC

Goal: Nurture VLSI engineers at the BUDding phase

Criteria:

- B.E. final year
 - EC, EE, CS & IT streams
- M.Tech final year
 - Microelectronics, VLSI, Computer tech
- Strong in digital, programming, exposure to VLSI a must.
- For CS streams - compilers/parsers, API related programming

Process:

- UNIV to send list of resumes
- CVC selects interns
- Identify project to work on
- Initial visit to CVC, ramp-up, problem statement
- Execution @ Univ, periodic reviews from CVC
- Final certification from CVC
- Choose 3 best projects at the end of the period (across all the UNIVs), offer prizes
- Result made visible to CVC's client list
- Consider for employment at CVC

Deliverables from CVC side:

- Project identification
- Ongoing guidance, review
- Any trainings as needed

Deliverables from Univ side:

- Student & guide identification
- Execution, full ownership

Timelines:

- Aug 2009 - May 2010

Contacts:

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